

FORM PTO-1449 SAMUELS, GAUTHIER & STEVENS LLP
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

7042
ATTORNEY DOCKET NO.

Zhang et al.
APPLICANT

07/16/2003
FILING DATE

10/620,395
SERIAL NO.

~~2816~~ **2819**
GROUP

Anh Q. Tran
EXAMINER

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|---------------------|--------------------|------|------|-------|----------|-------------------------------|
| | AA | | | | | |
| | AB | | | | | |
| | AC | | | | | |
| | AD | | | | | |
| | AE | | | | | |
| | AF | | | | | |

FOREIGN PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION YES NO |
|---------------------|--------------------|------------|---------|-------|----------|-----------------------|
| <u>AT</u> | AG 0766396 | 04/02/1997 | EP | | | YES |
| | AH | | | | | |
| | AI | | | | | |

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| EXAMINER INITIAL | | |
|---------------------|----|---|
| <u>AT</u> | AJ | "Voltage-Tolerant Monolithic L-Band GaAs SPDT Switch," Cooper et al., <i>IEEE MTT-S Digest</i> (1989). |
| <u>AT</u> | AK | "A Dual-Gate Shorted-Anode Silicon-on-Insulator Lateral Insulated Gate Bipolar Transistor with Floating Ohmic Contact for Suppressing Snapback and Fast Switching Characteristics," Oh et al., <i>Microelectronics Journal</i> 30 pp. 577-581 (1999). |
| | AL | |
| | AM | |
| | AN | |
| | AO | |

EXAMINER

DATE CONSIDERED

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Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.